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Complete If Known

Application Number	10/773,860-Conf. #7222
Filing Date	February 6, 2004
First Named Inventor	Rajesh Banginwar
Art Unit	2863
Examiner Name	T. S. Lau

Attorney Docket Number 30320/18023

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
TL		6,205,555	03-20-2001	Kageshima et al.	
TL		6,233,690	05-15-2001	Choi et al.	
TL		6,357,016	03-12-2002	Rodgers et al.	
TL		6,711,447	03-23-2004	Saeed	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁴
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
TL		Pering et al., "Dynamic Voltage Scaling and the Design of a Low-Power Microprocessor System," University of California at Berkeley, Electronics Research Laboratory, 6 pages (1998)	
TL		Govil et al., "Comparing Algorithms for Dynamic Speed-Setting of a Low-Power CPU," Computer Science Division, University of California, pp 13-26 (1995)	
TL		Grunwald et al., "Policies for Dynamic Clock Scheduling," Department of Computer Science, University of Colorado, 14 pages (2000)	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Examiner Signature	Ty Lau	Date Considered	6-15-06
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